

**Express Mail Label No. EV396082112US**

**APPLICATION DATA SHEET  
UNDER 37 CFR §1.76**

**(1) Inventor Information**

Inventor One Given Name::	Younglok
Family Name::	Kim
Postal Address Line One::	Sogang University, Rook K-540
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City::	Seoul
State or Province::	Korea
Postal or ZIP Code::	121-742
Citizenship Country::	Korea
Residence::	Seoul, Korea

**(2) Assignee Information**

Name Line One::	InterDigital Technology Corporation
Address Line One::	300 Delaware Avenue
Address Line Two::	Suite 527
City::	Wilmington
State or Province::	Delaware, U.S.A.
Postal or ZIP Code::	19801

**(3) Correspondence Information**

Customer No.::	24374
Name Line One::	John C. Donch Jr.
Name Line Two::	Volpe and Koenig, P.C., DEPT ICC
Telephone No.::	215-568-6400

**(4) Application Information**

Title Line One::	SIMPLIFIED BLOCK LINEAR EQUALIZER
Title Line Two::	WITH BLOCK SPACE TIME TRANSMIT
Title Line Three::	DIVERSITY
Total Drawing Sheets::	Three (3)
Drawing Type::	Formal
Application Type::	Continuation
Docket No.::	I-2-0181.2US

(5) Representative Information

Representative Customer No.: 24374

(6) Domestic Priority Information

This application is a:: Continuation of  
Application One:: 10/034,793  
Filing Date:: December 27, 2001

which is a:: Non-Provisional of  
Application Two:: 60/263,915  
Filing Date:: January 25, 2001